TRANSMITTAGOF INFORMATION DISCLOSURE STATEMENT Docket No.									
MAR 0 1 2004 Under 37 CFR 1.97(b) or 1.97(c)) M12.014									
In Re Application Jordan, et al.									
Serial No. 10/686,315	Filing Date October 15, 2003	Examiner	Group Art Unit 2812						
Title:									
MULTILEVEL ELEC	MULTILEVEL ELECTRONIC CIRCUIT AND METHOD OF MAKING THE SAME								
Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450									
	37 (CFR 1.97(b)							
1. The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.									
	37 (CFR 1.97(c)							
2. The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:									
☐ the statem	nent specified in 37 CFR 1.97(e)	1							
	OR								
☐ the fee se	t forth in 37 CFR 1.17(p).								
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TRANSMITTAL OF	INFORMATION DISCLOS	URE STATEMENT	Docket No.	
MAR 0 1 2004 (U	(2)	MI2.014		
In Re Application: Jo	ordan, et al.			
Serial No.	Filing Date	Examiner	Group Art Unit	
10/686,315	October 15, 2003			
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Vimoto	y, Suite 206	Dated: February 26, 2004		
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P10A/REV02

INFORMATION DISCLOSURE CITATION (Up everal sheets if necessary)				Docket Number (Optional) M12.014		Application Number				
				Applicant(s)						
					Jordan, et al.					
MAR U I ZUNA 7					Filing Date October 15,		Group Art Unit	2812		
U.S. PAT					ENT DOCUMENTS					
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		1	NanoNexus, www.nanonexus.com/pages/products, 11/08/2002, "Cost Effective Electrical Connections For Advanced IC's".							
EXAMINER				DATE CONSIDERED						
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